IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Art Unit: Not assigned Examiner: Not assigned Daisuke KATO et al.

Serial No: Not assigned (parent 10/053,524)

Filed: September 17, 2003

For: Semiconductor Storage Device

Formed to Optimized Test Technique

and Redundancy Technology

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sirs: 1. The information disclosure statement submitted herewith is being filed within three months of the filing date of the application other than a continued prosecution application, or within three months of the date of entry into the national stage of an international application, or before the mailing date of a first Office Action on the merits, or before the mailing of a first Office action after the filing of a request for continued examination under §1.114, whichever event occurs last. 37 C.F.R. §1.97(b). The information disclosure statement transmitted herewith is being filed after the period specified in §1.97(b), but before the mailing date of a final action under §1.113, or a notice of allowance under §1.311, or an action that otherwise closes prosecution in the application, whichever occurs first. A statement specified in §1.97(e) or a fee set forth in §1.17(p) is included. 37 C.F.R. §1.97(c). §1.97(e) STATEMENT I, the person signing below, state:

that each item of information contained in the information disclosure statement
was first cited in the attached communication from a foreign patent office in a
counterpart foreign application and that the communication is dated not more
than three months prior to the filing of the statement. 37 C.F.R. §1.97(e)(1).

OR.

that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification

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after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the statement. 37 C.F.R. §1.97(e)(2).

OR FEE Attached is a fee set forth in 37 C.F.R. §1.17(p) for submission of an information disclosure statement under §1.97(c). (\$180.00). 3. The information disclosure statement transmitted herewith is being filed after the period specified in §1.97(c), but before, or simultaneously with the payment of the issue fee. A statement specified in §1.97(e) and a fee set forth in §1.17(p) are included. 37 C.F.R. §1.97(d). §1.97(e) STATEMENT I, the person signing below, state: that each item of information contained in the information disclosure statement was first cited in the attached communication from a foreign patent office in a counterpart foreign application and that the communication is dated not more than three months prior to the filing of the statement. 37 C.F.R. §1.97(e)(1). OR that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the statement. 37 C.F.R. §1.97(e)(2). AND FEE Attached is a fee set forth in 37 C.F.R. §1.17(p) for submission of an information disclosure statement under §1.97(d). (\$180.00).

- 4. If it should be determined that for any reason either an insufficient fee or an excessive has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this petition is enclosed.
- 5. A list of <u>5</u> reference(s) is in the enclosed Form PTO-1449. Pursuant to 37 C.F.R. § 1.98(d), copies of the listed reference(s) are not provided since they were submitted in parent application Serial No. <u>10/053,524</u>, the benefit of the filing date of which is claimed herein under 35 U.S.C. § 120.

	NON-ENGLISH LANC	GUAGE REFERENCES						
		counterpart application. The search report n the relevancy of any non-English language						
	The specification incorporates comments on the relevancy of Non-English language references.							
	Set forth below are comments provided by the applicant's home country counsel the relevancy of non-English language references: N/A							
		Respectfully submitted,						
		HOGAN & NARTSON L.L.P.						
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FORM PTO-1	449	Docket Number (Optional) 81790.0297		Application Number Not assigned					
INFC	PRMATION DISCLOSURE CIT	Applicant Daisuke KATO et al.							
	(Use several sheets if necessary)	Filing Date September 17	Filing Date September 17, 2003		Group Art Unit Not assigned				
		U.S. PATEN	T DOCUMENTS	-					
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE			
	5,546,349	8/13/96	WATANABE et al.						
	5,615,164	3/25/97	KIRIHATA et al.						
	5,841,710	11-1998	LARSEN	365	200				
	6,396,748	05-2002	FUJITA	365	201				
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*- WW.			3.400						
	FC	REIGN PAT	ENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)			
	OTHER DOCUMENT	S (Including A	uthor, Title, Date, Perti	inent Pages,	Etc.)				
	Yohji WATANABE et al., "A 286 mm² 256Mb DRAM with x32 Both-Ends DQ", IEEE Journal of Solid-State Circuits, Vol. 31 No. 4, April 1996, pp 567-574.								
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EXAMINER		DATE CONSIDI	ERED						
EXAMINER: I	nitial if citation considered, whether or and not considered. Include copy of t	not citation is in	n conformance with MPE	EP § 609; Dra	w line through c	itation if not in			